

FIG. 1

40: CURRENT MIRROR CIRCUIT

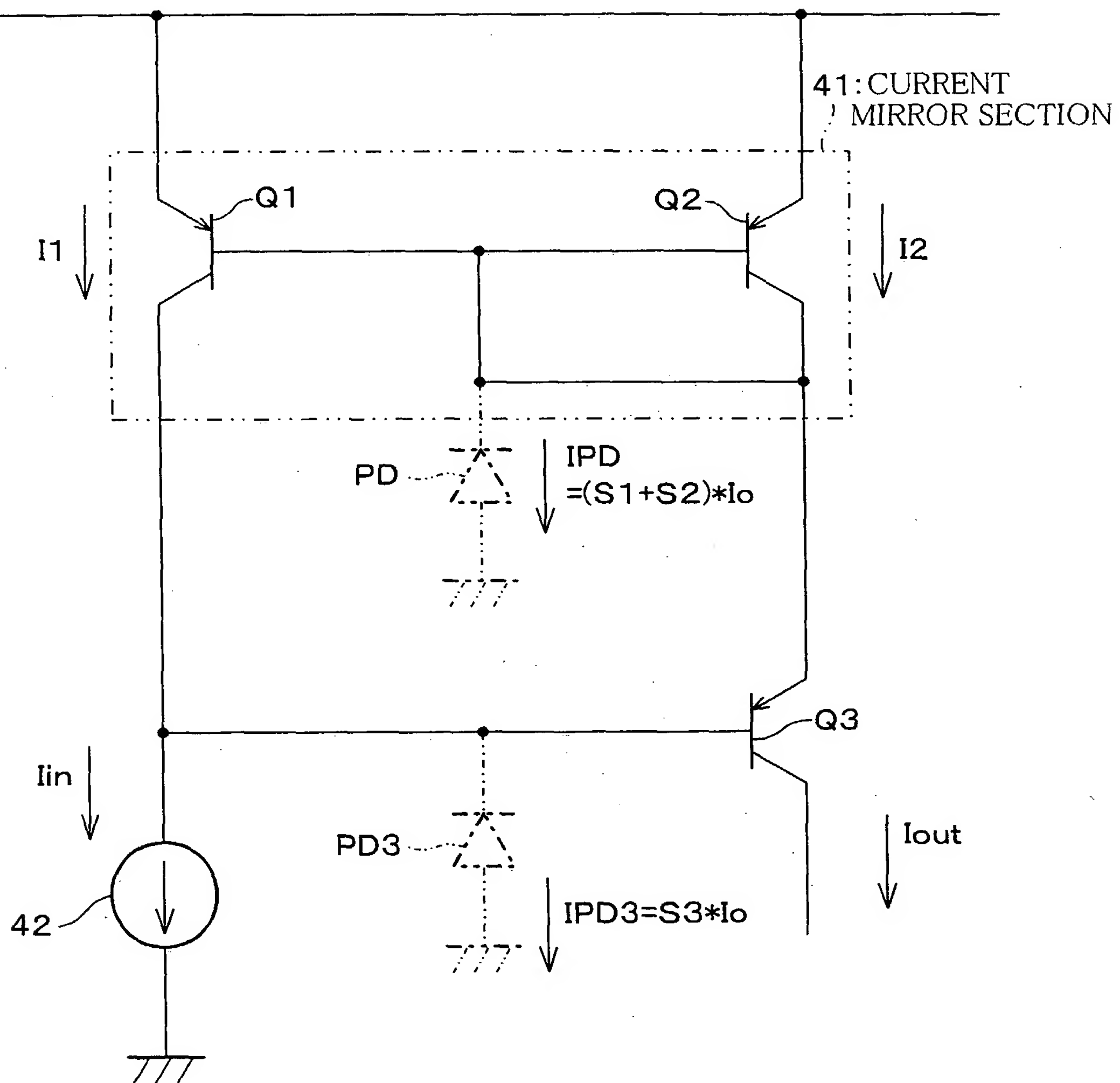


FIG. 2

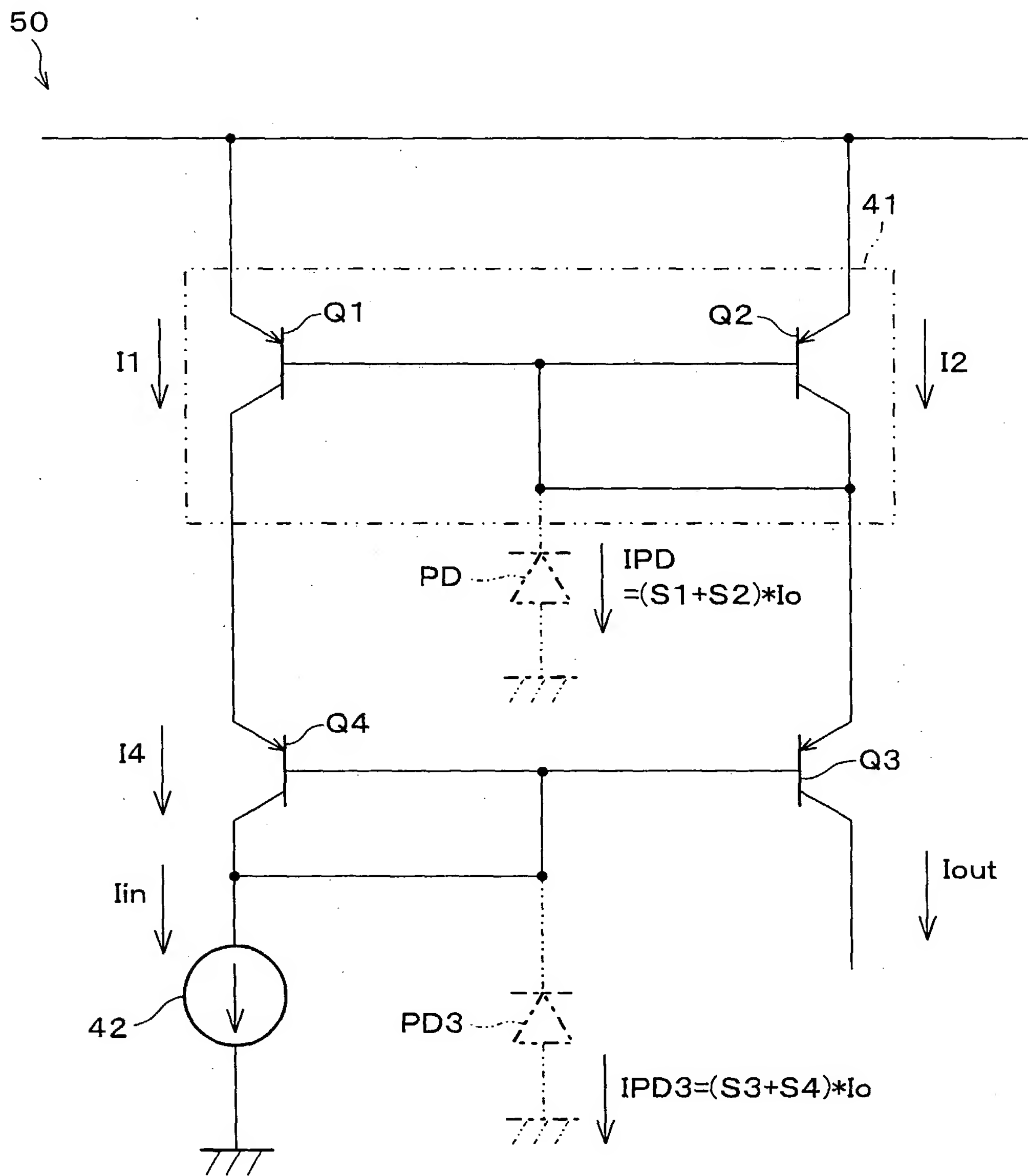


FIG. 3

60
↓

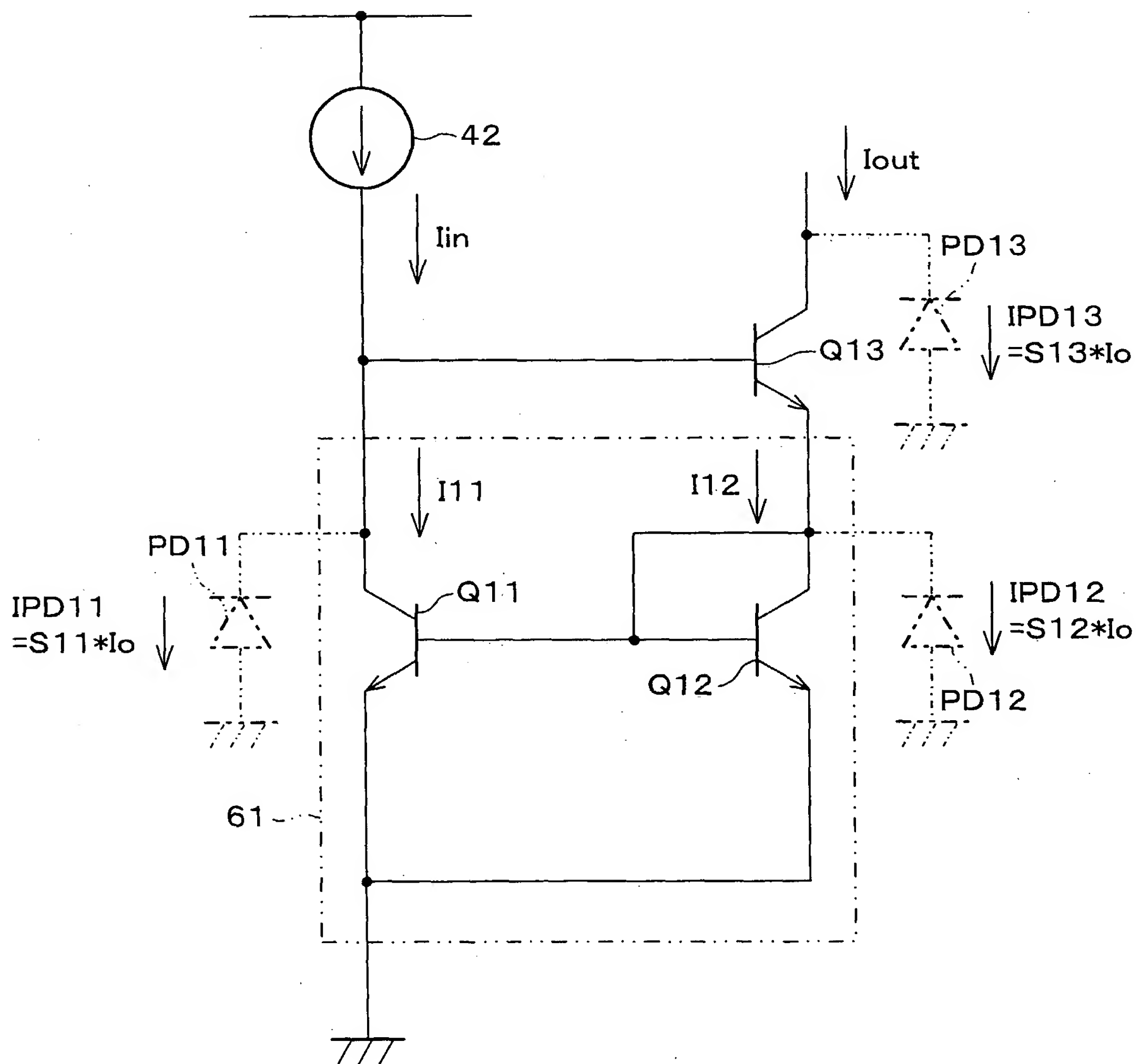


FIG. 4

70
↓

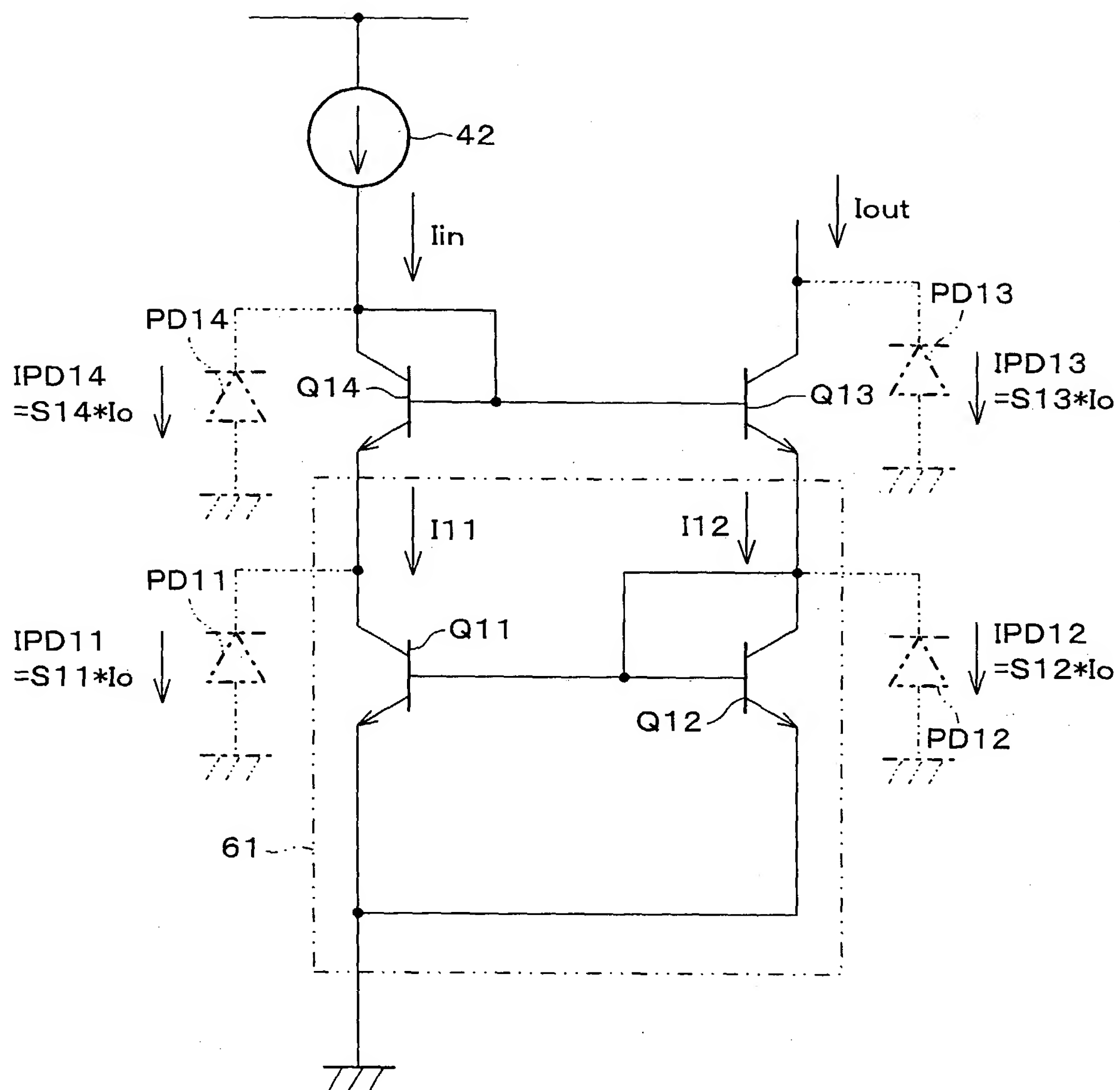


FIG. 5

80

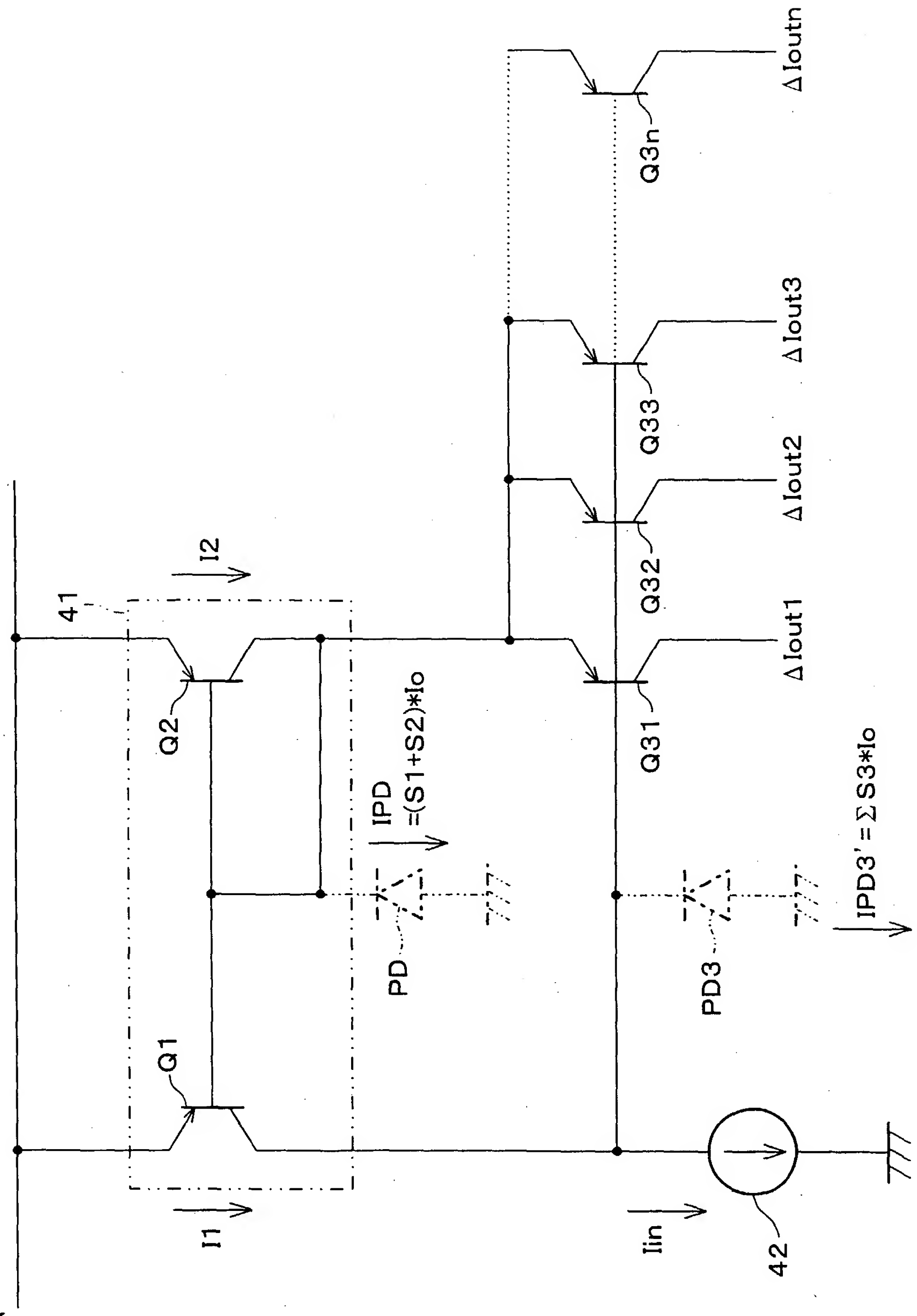


FIG. 6 (a)

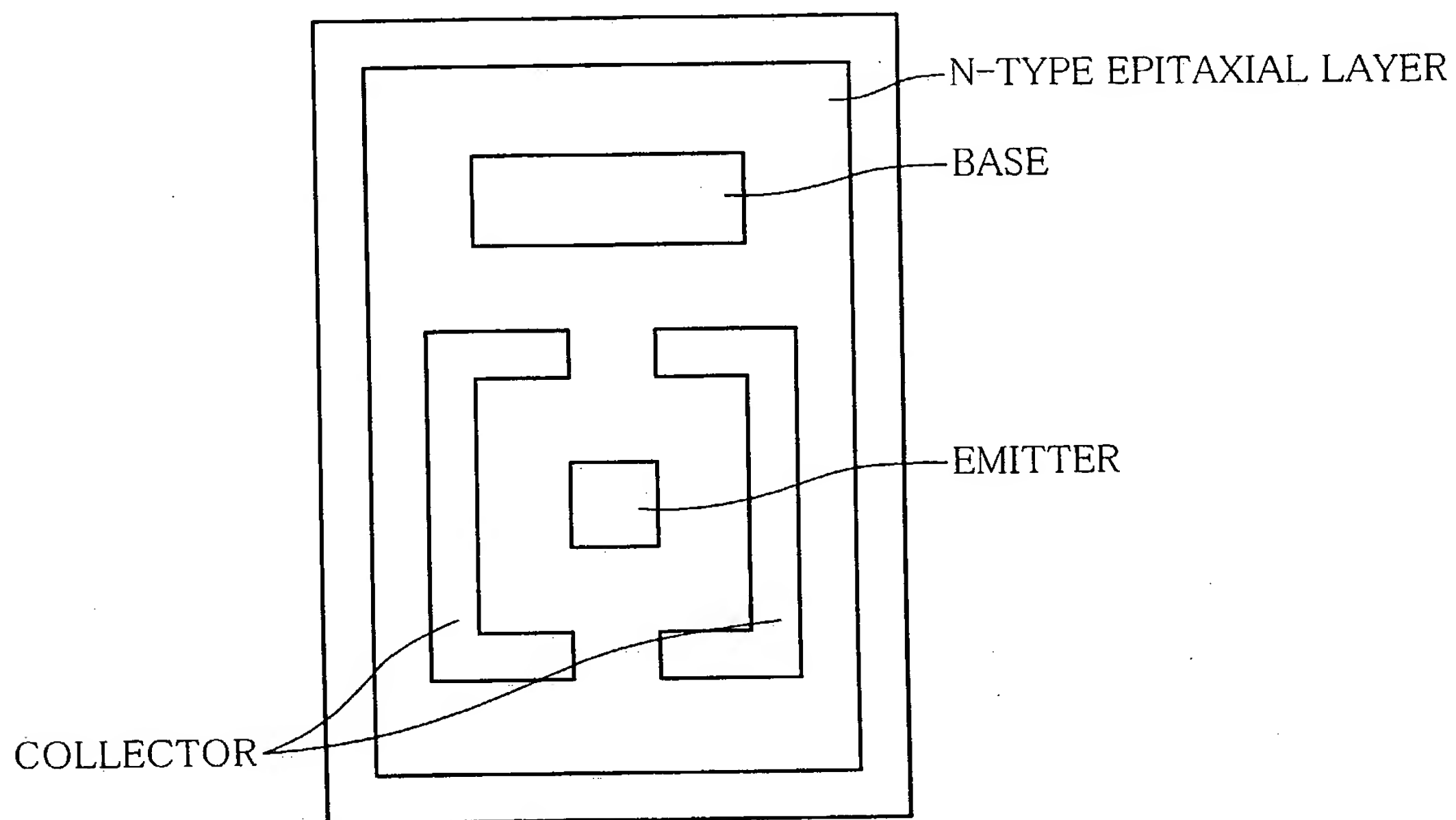


FIG. 6 (b)

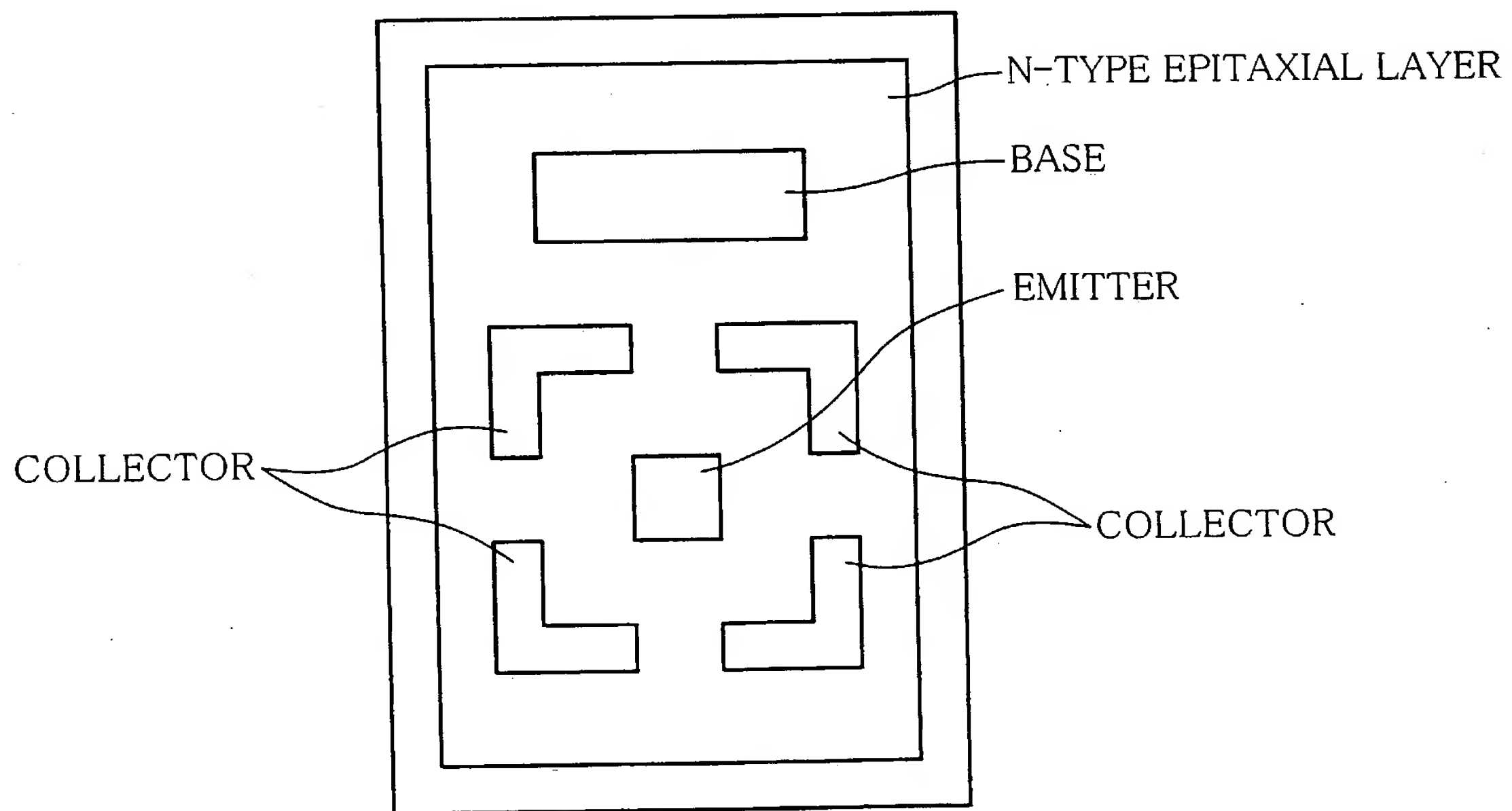


FIG. 7

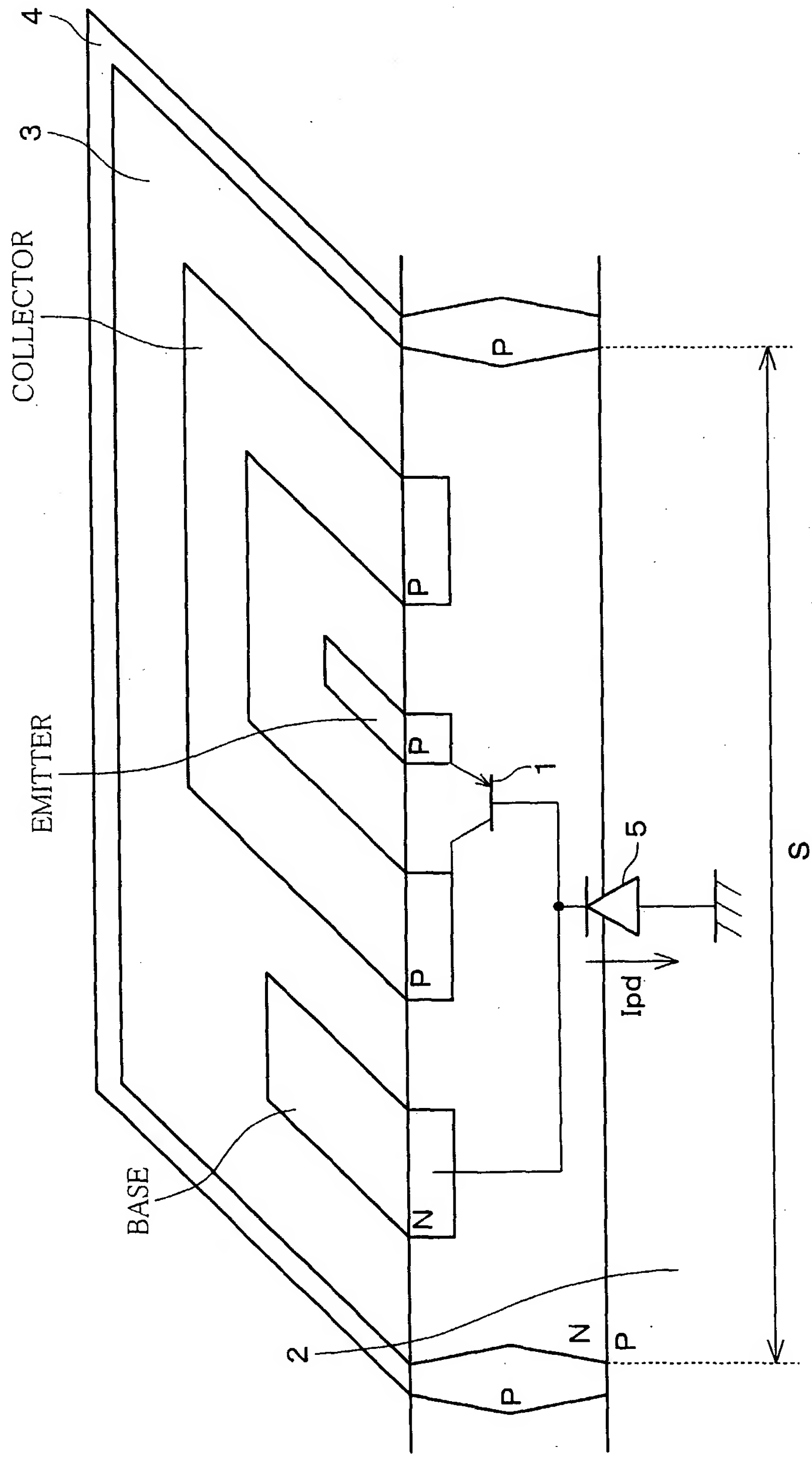


FIG. 8

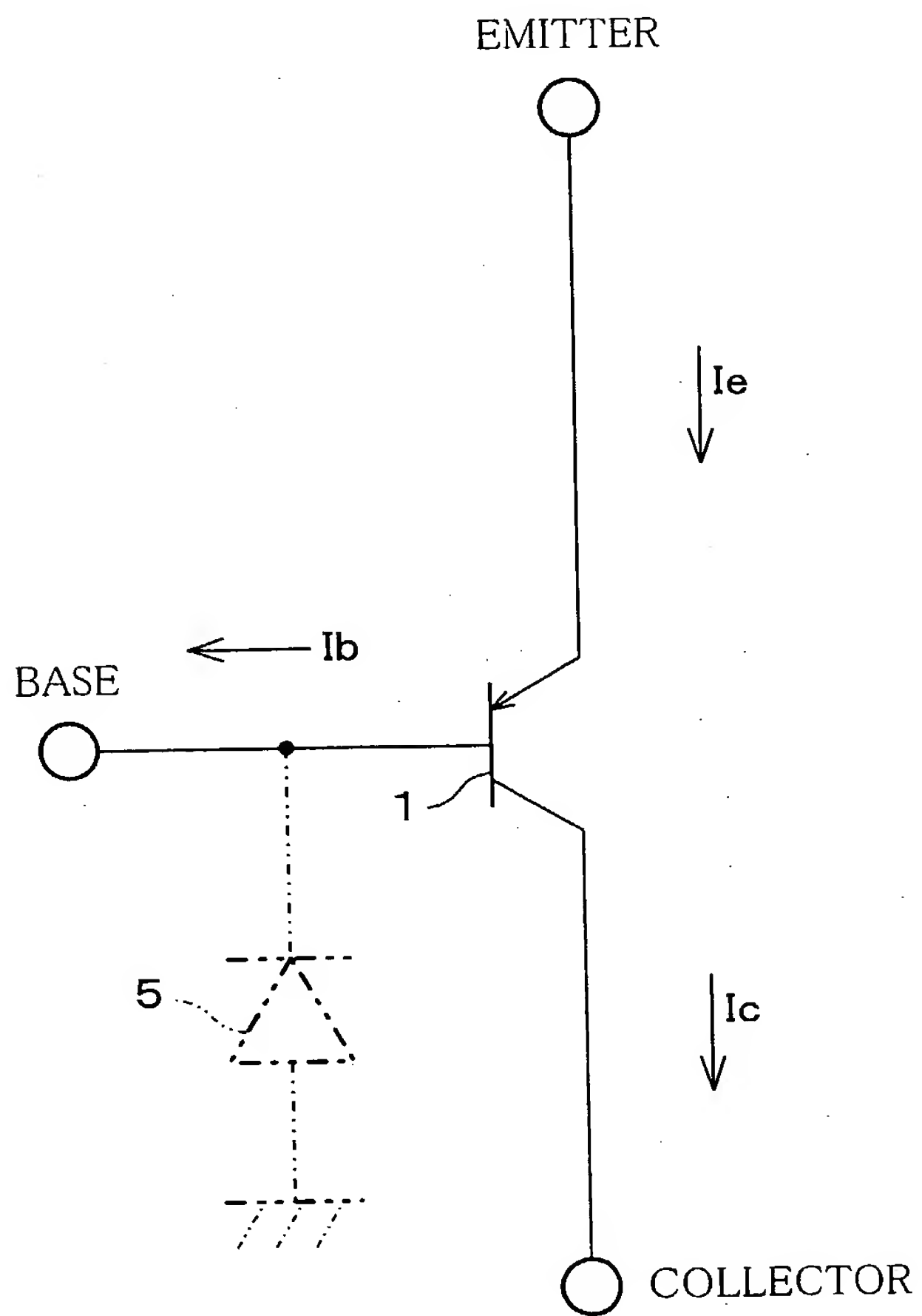


FIG. 9

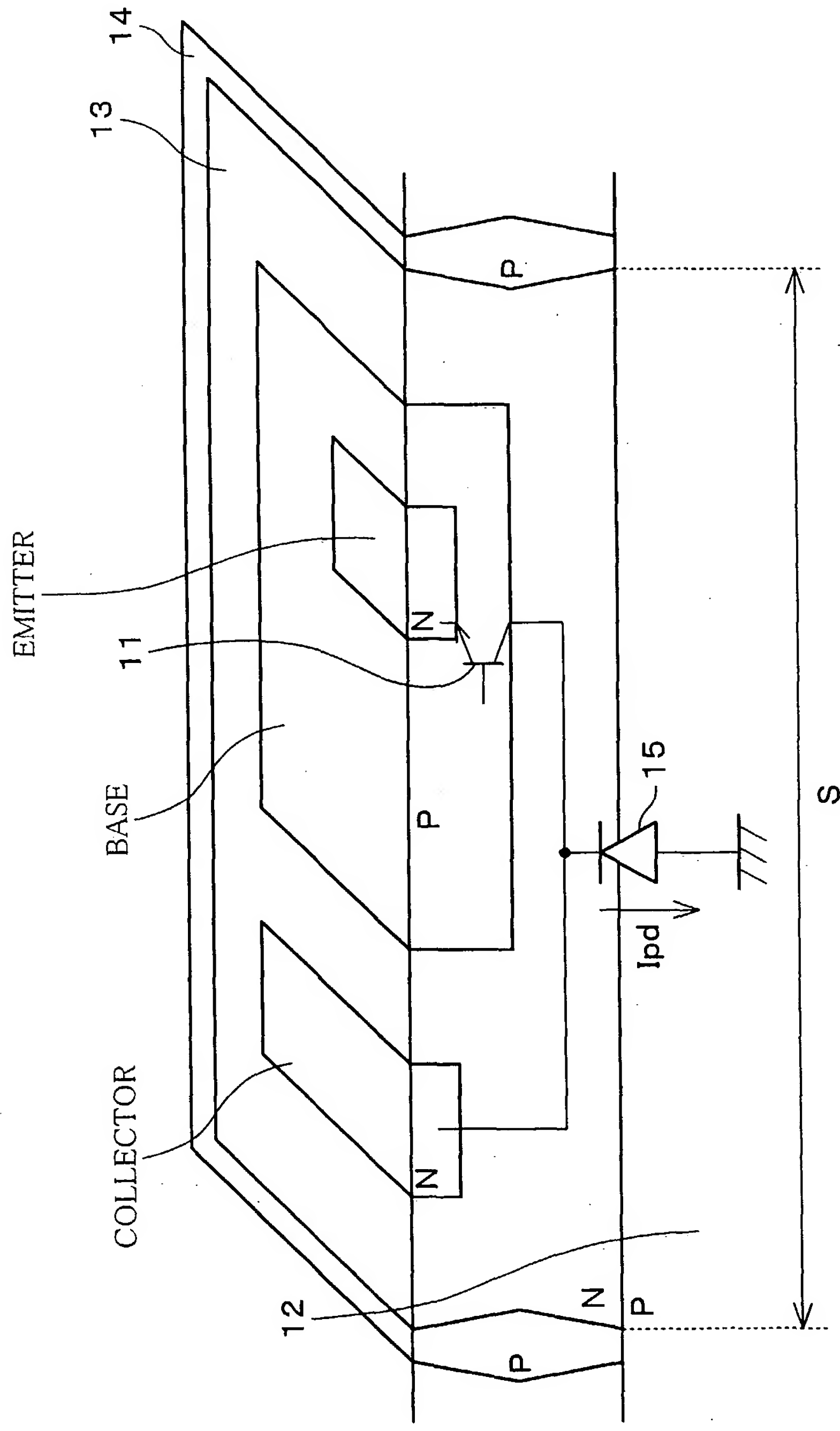


FIG. 10

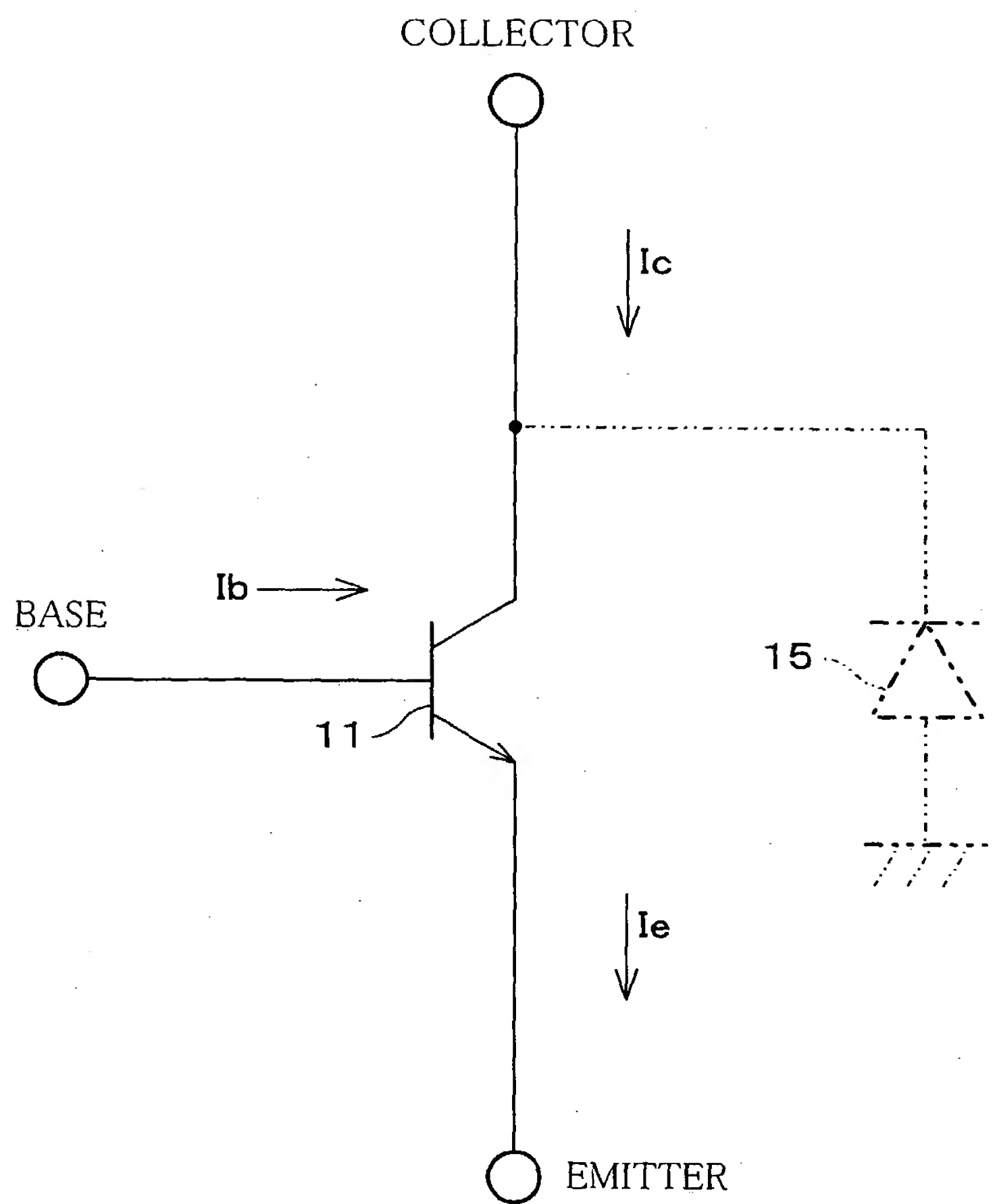


FIG. 11

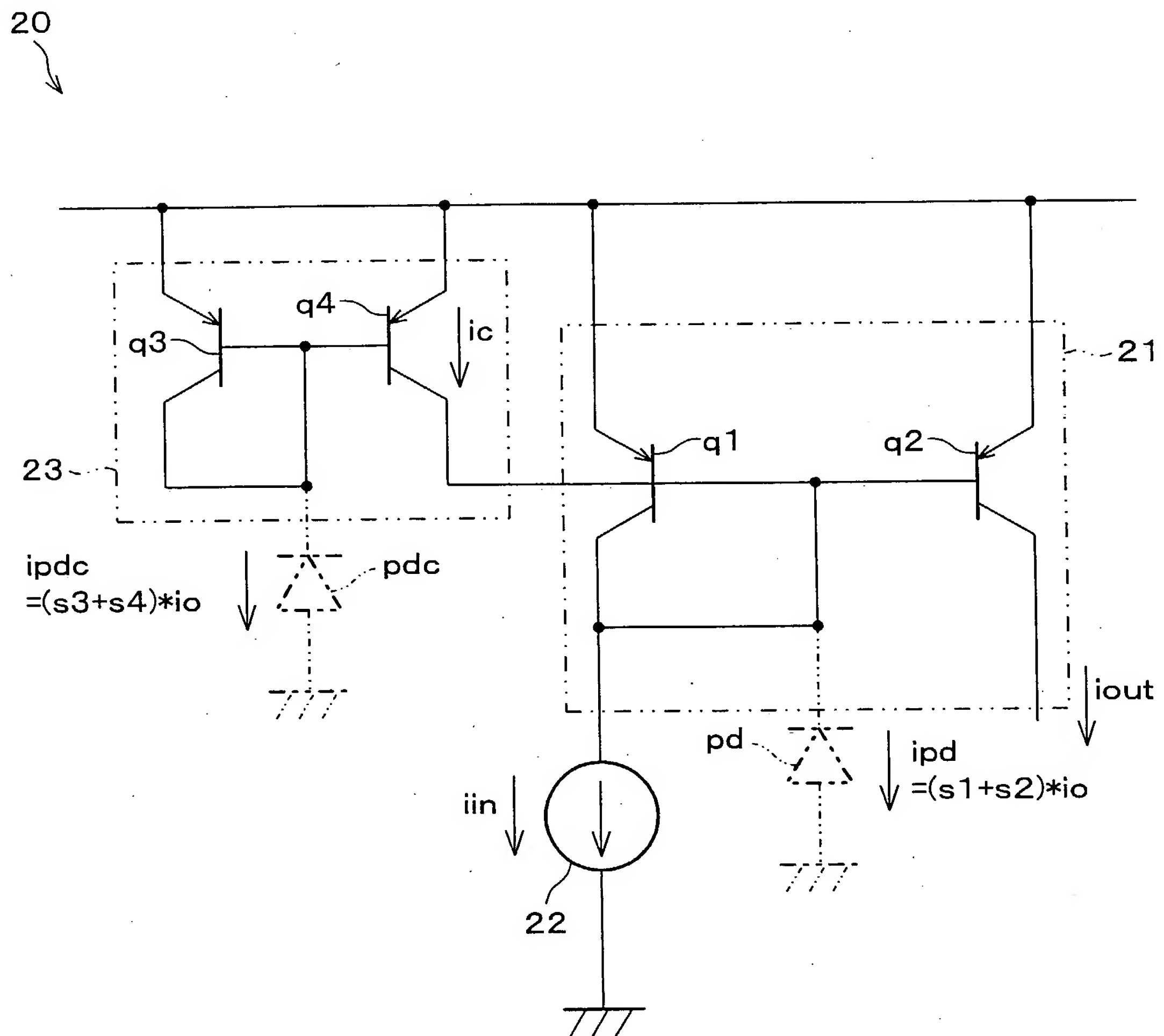


FIG. 12

